

Analytical Model of a “Split-Coil” for Implementation of novel Type of Receive Coil in Magnetic Resonance Imaging

Enrico Pannicke^{1,3}, Oliver Speck^{2,3}, Ralf Vick²

Abstract—In this work implementing MR receive coils from overlapping traces is investigated. Such a configuration is known from Microstrip transmission line (MTL) coils, which are basically used in Magnetic resonance - ultra high field (MR-UHF) imaging as TX-RX volume coils. Applications at lower field strengths are less common, because the required electrical length is more difficult to satisfy as the frequency decreases. Overlapping traces are already known for lower field strengths like 1.5 T or 3 T. Such configurations provide the ability of reducing the number of lumped on such a coil becoming more flexible. To investigate such a flexible coil the overlap is extended to much larger degree and it will be shown that this setup can be modeled as classical transmission line. An analytical model is developed and verified with simulations providing accurate calculations of the input impedance. This allows a reliable derivation of its parameters, which simplifies implementation of such coils.

I. INTRODUCTION

In this work, the possibility of building conductor loops for MR receiving coils using overlapping copper traces is investigated. Such a configuration is known from Microstrip transmission line (MTL) coils, which are basically used in Magnetic resonance - ultra high field (MR-UHF) imaging as TX-RX volume coils. They differ significantly in geometry from circular loops and consist of straight line elements, tuned to an electrical length of $\lambda/4$ [1] [2].

Their ease of manufacture, good performance and reasonable coverage with a comparably small number of elements recommends it as a plausible alternative. Applications at lower field strengths are less common, because the required electrical length is more difficult to satisfy as the frequency decreases. In this work, the possibility of building MR receive coils based on overlapping traces is investigated. Such configurations are already known from other studies [3] for lower field strengths like 1.5 T or 3 T to create flexible MR coils.

In contrast to former studies the overlap is extended to much larger degree and it will be shown that this setup can be modeled as classical transmission line. Based on this insight an analytical model could be developed and verified with simulations providing accurate calculations of the input impedance. This allows a reliable derivation of its parameters and a efficient tuning an matching procedure.

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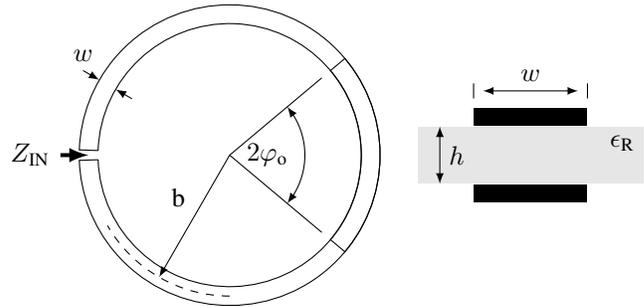


Fig. 1. Geometrical setup of the proposed “SplitCoil”. The in plane shift of the traces is only for better visualization.

II. METHODS

The so called “Split-Coil” is shown in Fig. 1 and consists of two circular arcs of copper traces, with loop radius b and trace width w overlapped by a degree of $2\varphi_0$. Both traces are separated by a dielectric, with relative permittivity ϵ_R and thickness h . The corresponding input impedance as a function of the normalized electrical length kb for different degrees of overlapping is demonstrated in Fig. 2. The parameter kb normalizes the freespace wavenumber k with the loop radius b .

Especially for low frequencies, the capacitive impedance $Z_{IN} \approx 1/j\omega C_G$ of the “Split-Coil” differs fundamentally from the inductive curve of a normal conductor loop $Z_{IN} \approx j\omega L_S$. Fig. 2 demonstrates clearly that the capacitance C_G is influenced by the degree of overlap. In principle, the overlapping conductor paths can be modeled as a concentrated capacitor. The drawback of this approach is that any propagation effects along the line are neglected, thus representing a quasi-static approximation. In the following, it is investigated whether a lumped approach is sufficient to adequately model the present arrangement or whether a much more complex approach must be used. Therefore two models are developed and compared with each other regarding their accuracy and predictability.

A. 1. Model: Concentrated Capacitor

A “quasi-static” simplification of the “Split-Coil” is shown in Fig. 3. The overlapping traces form a lumped capacitor, which is located opposite to the feeding port. This capacitance C_P can be calculated using the overlapping area A , the dielectric properties of the substrate ϵ_R , and the substrate

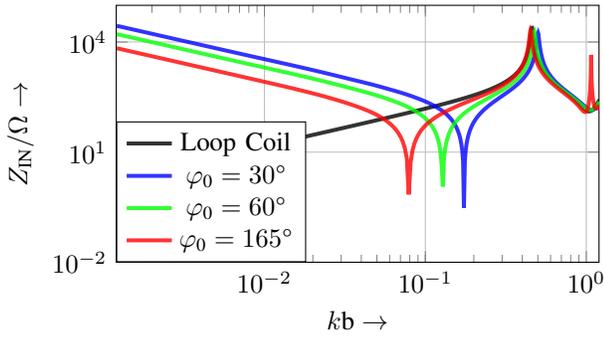


Fig. 2. Input impedance Z_{IN} of the proposed “Split-Coil” for several degrees of overlap φ_o ($w = 5$ mm, $b = 57.5$ mm, $d = 1$ mm, $\epsilon_R = 1$) in comparison to a normal loop without overlap.

height h :

$$C_P = \frac{A \cdot \epsilon_0 \cdot \epsilon_R}{h}$$

Increasing the overlap thus leads to an increase in capacitance by enlarging the cross-sectional area, which corresponds well with the course of impedance in the low frequency region. The cross-sectional area of the capacitor is given by the degree of overlap (see Fig. 3) and C_P is calculated by

$$C_P = \frac{2 \cdot \varphi_o \cdot b \cdot w \cdot \epsilon_0 \cdot \epsilon_R}{h} \quad (1)$$

Concentrating the distributed capacitance into one lumped element leads to a circular loop without overlap. As shown in Fig. 3 inserting a lumped capacitance in the mid of the former overlap, the circular loop is becoming a two-port network, which is given by:

$$[\mathbf{Y}] (\varphi_1) = \begin{bmatrix} Y_0 & Y_1 \\ Y_1 & Y_0 \end{bmatrix} \quad (2)$$

The parameters Y_0 and Y_1 are calculated by [4]

$$Y_0 = Y_{IN} (a, b) \quad (3)$$

$$Y_P = Y_{IN} (a, b) \cdot \frac{I_1}{I_0} \quad (4)$$

, where the current I_1 is located at the position $\varphi_1 = \pi$ along the circumference of the loop. The input admittance $Y_{IN} (a, b)$ and current $I_{0/1}$ are calculated according to [5], with the equivalent wire radius $a = w/4$ [6].

B. 2. Model: Transmission-Line

The “non static” approximation considers the overlapping traces as symmetric transmission line, which is connected at $\varphi_1 = \pi - \varphi_o$ and $\varphi_2 = \pi + \varphi_o$ to the circular loop. Inserting the corresponding ports on the loop gives at three-port matrix (See Fig. 4 A), which is given by:

$$[\mathbf{Y}] = \begin{bmatrix} Y_0 & Y_1 & Y_2 \\ Y_2 & Y_0 & Y_1 \\ Y_1 & Y_2 & Y_0 \end{bmatrix}. \quad (5)$$

Considering the reference impedance Z_{ref} , the admittance matrix is transformed into a S-parameter representation

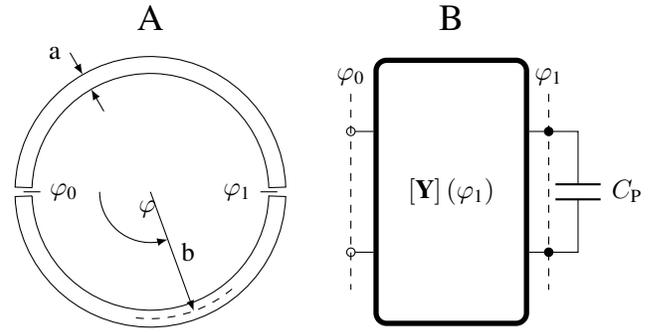


Fig. 3. Simplified model by replacing the overlapping traces with a lumped capacitor C_P .

$[\mathbf{Y}] \rightarrow [\mathbf{S}]$.

The transmission line is inserted between port φ_1 and φ_2 and taking into account that the orientation of the ports is given by φ , we obtain the interconnection of the three-port and two-port shown in Fig. 4 B. To account for the fringe fields of the open stubs at the ends of the traces, additional capacitors C_F are modeled at the connecting ports.

The S-parameter matrix of the transmission line is calculated by

$$[\mathbf{S}_{MTL}] = \begin{bmatrix} \frac{r \cdot (1-p^2)}{1-r^2 \cdot p^2} & \frac{p \cdot (1-r^2)}{1-r^2 \cdot p^2} \\ \frac{p \cdot (1-r^2)}{1-r^2 \cdot p^2} & \frac{r \cdot (1-p^2)}{1-r^2 \cdot p^2} \end{bmatrix} \quad (6)$$

$$r = \frac{Z_0^{DM} - Z_{ref}}{Z_0^{DM} + Z_{ref}} \quad (7)$$

$$p = e^{-j\omega(l_{MTL}/v_{MTL})} \quad (8)$$

, where Z_0^{DM} is its characteristic impedance, l its geometrical length, v_{MTL} its propagation velocity and Z_{ref} the reference impedance of both ports.

The characteristic impedance of the symmetric transmission

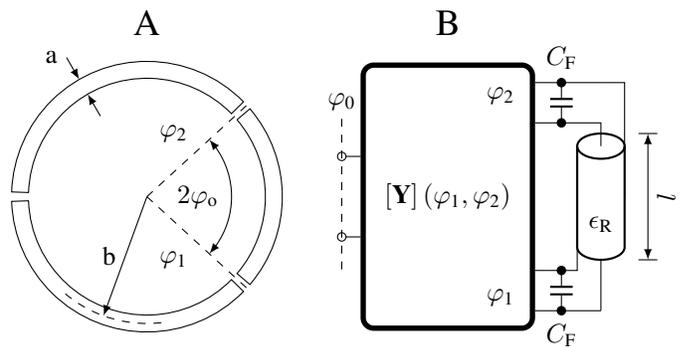


Fig. 4. Second model of the “Split-Coil”, which replaces the overlapping traces by a transmission line.

line can be calculated from the characteristic impedance of an unsymmetrical microstrip-line Z_0^{CM} . This is done by introducing a symmetry plane between both traces, which is demonstrated in Fig. 5. The mirroring at this plane allows

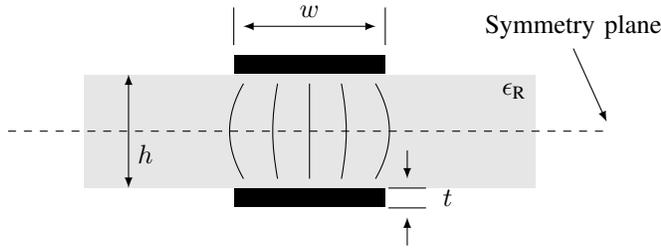


Fig. 5. Conductors within the overlap area. The symmetry plane of the arrangement allows the calculation of the characteristic impedance based on an unbalanced microstrip line.

the representation of the symmetric transmission line as a parallel connection of two unsymmetrical transmission lines in particular microstrip lines. This leads to

$$Z_0^{\text{DM}} = \frac{Z_0^{\text{CM}}}{2}. \quad (9)$$

, where Z_0^{CM} is calculated by [7]

$$Z_0^{\text{CM}} = \frac{Z_{F0}/\epsilon_{r,\text{eff}}}{\frac{w_{\text{eff}}}{h_{\text{MTL}}} + 1.393 + \frac{2}{3} \ln\left(\frac{w_{\text{eff}}}{h_{\text{MTL}}} + 1.444\right)}. \quad (10)$$

The effective trace width w_{eff} and effective permittivity $\epsilon_{r,\text{eff}}$ are reduced values of the corresponding parameter, due to the layered dielectric and Z_{F0} is the free space wave impedance. For the proper calculation of Z_0^{CM} the height of unsymmetrical transmission line h_{MTL} is given by

$$h_{\text{MTL}} = \frac{h}{2}. \quad (11)$$

As the effective height h_{MTL} is reduced it seems reasonable that $w/d > 1$ is always satisfied. The effective trace width w_{eff} and permittivity $\epsilon_{r,\text{eff}}$ are calculated by

$$w_{\text{eff}} = h_{\text{MTL}} \cdot \left(\frac{w}{h_{\text{MTL}}} + \frac{5 \cdot t}{4\pi h_{\text{MTL}}} \cdot \left(1 + \ln\left(\frac{2h}{t}\right) \right) \right) \quad (12)$$

$$\epsilon_{r,\text{eff}} = \frac{\epsilon_r + 1}{2} + \frac{\epsilon_r - 1}{2} \cdot \frac{1}{\sqrt{1 + 12h_{\text{MTL}}/w}} - \frac{\epsilon_r - 1}{4.6} \cdot \frac{t/w}{\sqrt{w/h_{\text{MTL}}}} \quad (13)$$

Fig. 6 demonstrates the dependence of the characteristic impedance of a microstrip line on the thickness and permittivity of the substrate.

The length of the microstrip line is calculated according to the overlap

$$l_{\text{MTL}} = 2 \cdot \varphi \cdot b, \quad (14)$$

and the velocity is reduced by $\epsilon_{r,\text{eff}}$:

$$v_{\text{MTL}} = \frac{1}{\sqrt{\epsilon_0 \cdot \epsilon_{r,\text{eff}} \cdot \mu_0}}. \quad (15)$$

The input impedance Z_{IN} is acquired by terminating [S] with [S_{MTL}] as shown in Fig. 4.

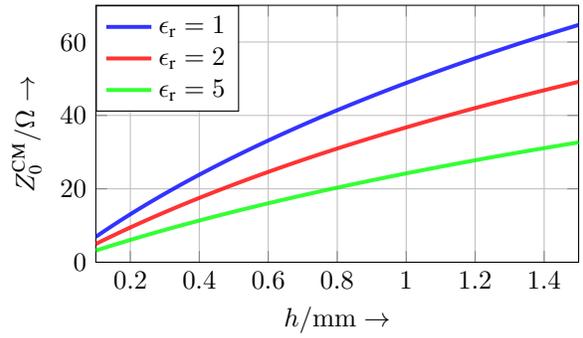


Fig. 6. Characteristic impedance of a microstrip line as a function of spacing between the traces h ($w = 5$ mm). for several relative permittivities ϵ_r .

C. Calibration of parameters

As both models are based on prior calculations of the input admittance $Y(a, b, \epsilon_R)$ of a conventional printed circular loop, its effective relative permittivity $\epsilon_{R,\text{eff}}$ has to be determined [8]. For the second model, the determination of the capacitor C_F is also required. For each configuration, two reference simulations are performed to determine the respective parameters. These included a short- and open-circuit ‘‘Split-Coil’’. The latter configuration is achieved by an overlap angle of $\varphi_o = 0^\circ$.

The simulation of the short-circuit ‘‘Split-Coil’’ is used to calibrate the effective permittivity $\epsilon_{R,\text{eff}}$ by fitting $Y(a, b, \epsilon_R)$ to the simulation results by the means of ‘‘Differential Evolution’’ (DE) optimization [9].

For the fringe capacitance C_F the open circuit simulation is used. According to Fig. 4 (A) the open circuit is achieved by $\varphi \rightarrow 0$ and removal of the transmission line, which puts the fringe capacitance’s C_F on both ports into series. Therefore the equivalent Fig. 4 (A) is transferred into Fig. 3 (A) with $C_P = C_F/2$. With this insight the fringe capacitance can be determined by fitting the representation in Fig. 3 (B) to the results of the simulation as demonstrated in Fig. 7.

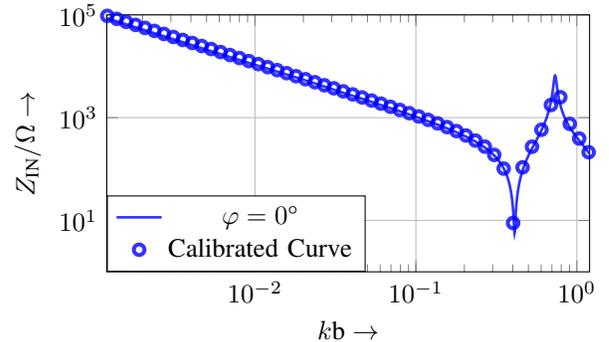


Fig. 7. Determination of C_F by DE-optimization to an open ‘‘Split-Coil’’ ($\varphi = 0^\circ$).

D. Comparison of both models

To evaluate both models, they are compared with simulated data in terms of their accuracy and the predictability

of the necessary parameters. The parameters of the simulated “Split-Coil” are summarized in Tab. I. An open-Source FDTD-Solver (openEMS, [10]) was used for the EM-Simulations of an unloaded and loaded “Split-Coil”. The latter includes a phantom ($\epsilon_R = 80$, $\kappa = 0.46 \text{ S m}^{-1}$) in a distance $d = 5 \text{ mm}$ below the “Split-Coil”.

To demonstrate the feasibility of the approach tuning and matching of the “Split-Coil” was conducted based on the best fitting model and validated by EM-simulations. The parameter for these “Split-Coil” are found in Tab. I.

| Parameter | Value | Parameter | Value |
|-------------------|---------|--------------|--------|
| Model validation | | | |
| b | 5.75 cm | h | 1 mm |
| a | 5 mm | ϵ_R | 1...5 |
| Matching & Tuning | | | |
| b | 6.75 cm | h | 0.1 mm |
| a | 2.5 mm | ϵ_R | 2.5 |

TABLE I
PARAMETERS OF THE “SPLIT-COIL” FOR DEMONSTRATING THE PERFORMANCE OF BOTH MODELS.

III. RESULTS

The result for the first modeling method can be seen in Fig. 8. The curve tends to be more correct for higher frequencies than for the low frequency range. Especially around the first minimum large discrepancies are apparent between simulation and model, which are increased with enlarged degree of overlap.

Additional resonance became apparent in the input impedance for increased electrical lengths of the overlap, which is shown in Fig. 9. These are not correctly reproduced by the lumped element model. Thus the model produces discrepancies in the lower and upper frequency range.

In Fig. 10 the results for the second model in comparison to

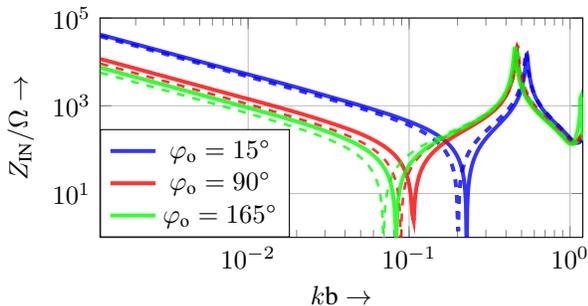


Fig. 8. Simulated (line) and calculated (dashed) input impedance Z_{IN} of the “Split-Coil” according to the first model (lumped element).

numerical data is shown. In contrast to the previous results, decisive features can be reproduced over the entire frequency range with the second model (i.e. see Fig. 9). The parameters determined by the outlined calibration procedure are summarized in Tab. III. It should be noted that the calibrated values

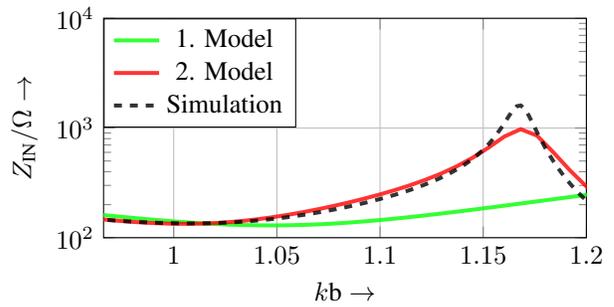


Fig. 9. Numerical (line) and calculated (marks) results of input impedance Z_{IN} of the “Split-Coil” in the high frequency range.

for $b = 57.5 \text{ mm}$ could be used for further setups of different loop radii. Obviously the fringe capacitance C_F is scaled by the relative permittivity ϵ_R .

The corresponding results for $\epsilon_R = 5$ is shown in Fig. 11. As before, regardless of the overlap, the calculated impedance demonstrates a good agreement with numerical results. This is also true if the “Split-Coil” is loaded with a phantom, which is demonstrated in Fig. 12.

The results for the matching and tuning of a larger “Split-

| b | 5.75 cm | h | 1 mm | w | 1 mm |
|------------------|---------|---------------------------|------|-------|---------|
| $\epsilon_R = 1$ | | $\epsilon_{R,\text{eff}}$ | 1 | C_F | 0.36 pF |
| $\epsilon_R = 5$ | | $\epsilon_{R,\text{eff}}$ | 1.3 | C_F | 1.8 pF |

Coil” ($b = 6.75 \text{ cm}$, $h = 0.1 \text{ mm}$, $a = 2.5 \text{ mm}$, $\epsilon_R = 2.5$) are demonstrated in Fig. 13. A required overlap of $\varphi_0 = 21^\circ$ could be extracted from the model for proper tuning ($\text{Re}\{Z_{IN}\} = 50 \Omega$). The matching was achieved by capacitive compensation with extraction of parameters from the proposed model as well.

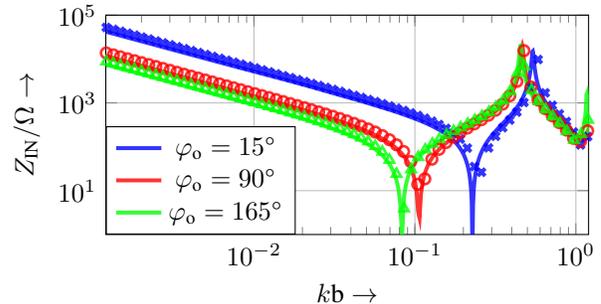


Fig. 10. Simulated input impedance Z_{IN} (line) for different overlap angles φ and $\epsilon_R = 1$ compared to the results of the second model (marks).

IV. DISCUSSION

Two models for the proposed “Split-Coil” were demonstrated and evaluated. The first model inherits the advantage of its simplicity, since $[\mathbf{Y}]$ has to be calculated only once no matter which overlap is required.

A more detailed comparison for an overlap of $\varphi = 30^\circ$ is shown in Fig. 14. Although the calculated capacitance $C_P = 5.33 \text{ pF}$ corresponds well to the impedance of the total capacitance Z_C in the low frequency range, the resonance

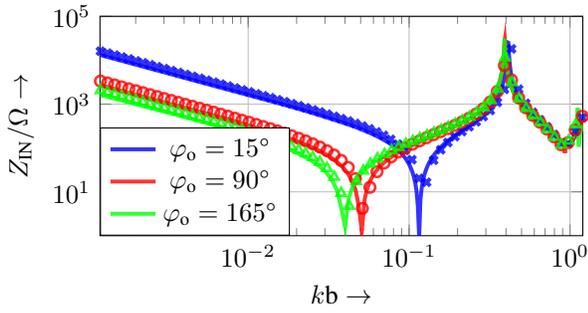


Fig. 11. Simulated input impedance Z_{IN} (line) for different overlap angles φ and $\epsilon_R = 5$ compared to the results of the second model (marks).

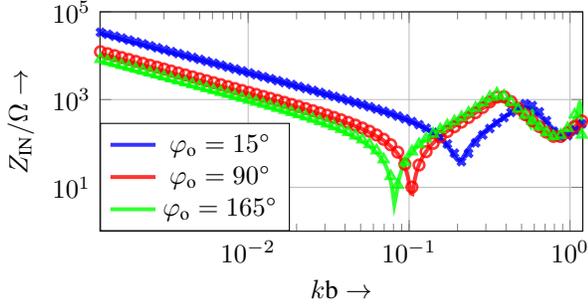


Fig. 12. Simulated input impedance Z_{IN} (line) of a loaded “Split-Coil” (Phantom: $\epsilon_R = 80$, $\kappa = 0.46 \text{ S m}^{-1}$) for different overlap angles φ and air substrate ($\epsilon_R = 1$) compared to the results of the second model (marks).

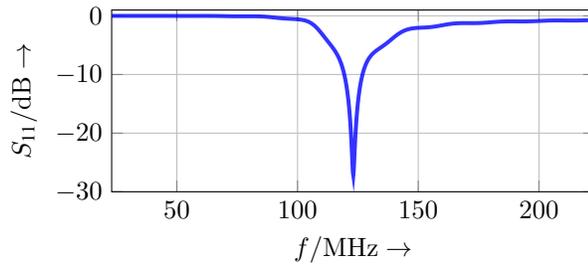


Fig. 13. Simulated reflection coefficient S_{11} of a “Split-Coil” with a overlap of $\varphi_o = 21^\circ$.

frequency of the first minimum f_0 is not predicted properly by the first model.

The increasing deviations for larger overlaps indicate that the propagation effects along them are not negligible, which is clearly evident for higher frequencies where additional resonances appear in the input impedance curve. These resonance are not reproduce by lumped capacitors, which is clearly demonstrated in Fig. 9.

The second model provides superior agreement with numerical results over the whole frequency range. Most parameters can be predicted by already existing equations for a microstrip-line. The effective relative permittivity of a printed loop on a substrate $\epsilon_{R,\text{eff}}$ and the fringe capacitance C_F have to be calibrated. A proper strategy for this could be presented, but still represents one of the most important drawbacks of the method as it still relies on additional simulations or measurements. Preliminary results seems to indicate that the fringe capacitance C_F is independent on the loop radius b , which simplifies the procedure. This seems reasonable as

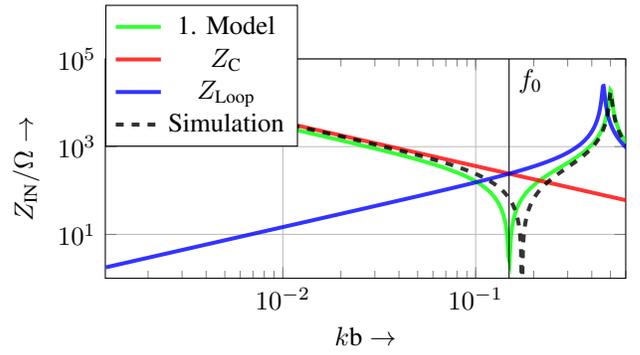


Fig. 14. Input impedance Z_{IN} of the “Split-Coil” for a degree of overlap $\varphi = 30^\circ$ calculated by the lumped element approach compared to a reference simulation.

the capacitance between the stub and opposing trace should depend on h , ϵ_R and w , which is subject to future studies. For use as a receive coil during MR imaging, the coils need to be matched to the a certain reference impedance (typically $Z_{\text{ref}} = 50 \Omega$) to preserve the intrinsic SNR as much as possible [11]. As the input impedance is adjustable by geometrical modifications of the “Split-Coil” the demonstrated models serve as important mean to optimize its parameters with respect to a specific reference impedance. A first prove of concept of its feasibility was demonstrated in these work. Further studies will concentrate on the aspect of calibration, as the need of preliminary simulations is one of the major obstacles for an efficient workflow.

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